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In re: Enrique Musoli et al Case: P3842 Art Unit: 2153
Application No.: 09/964.827 Filing date: 09/25/2001 Examiner: Glenton Burgess
Subject: Method and Apparatus for Overflowing Data Packets to a Software-Controlled Memory When They do not Fit into a Hardware-Controlled Memory

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Art Unit 2153 Examiner Glenton Burgess

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Serial No.: 09/964,827
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To: The Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Dear Sir:

Information Disclosure Statement under 37 C.F.R. §1.97

Enclosed herewith is an Information Disclosure Statement (IDS) by Applicant listing twenty nine (29) references. Under §1.98(d) no reference copies are required as they have been provided in priority application s/n 09/737,375. Applicant desires that the references be entered in the above-mentioned case as relevance dictates.

As prosecution has not begun in this case, in accordance with rule 1.97(b)(3), no additional fee is required. Please charge any deficiency or credit any overpayment to Deposit Account No. 50-0534.

Respectfully submitted,
Enrique Musoll et al.
by


Donald R. Boys
Reg. No. 35,074

Central Coast Patent Agency, Inc.
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PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 3

Complete if Known

Application Number	09/964,827
Filing Date	09/25/2001
First Named Inventor	Enrique Musoli
Art Unit	2153
Examiner Name	Glenton Burgess
Attorney Docket Number	P3842

Examiner Initials*	Cite No. ¹	U. S. PATENT DOCUMENTS			
		Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 6088745 A *	07-11-2000	Bertagna et al.	
		US- 6157955 A *	12-05-2000	Narad et al.	
		US- 6189745 B1 *	01-02-2001	Liu et al.	
		US- 6228680 B1 *	05-01-2001	Boucher et al.	
		US- 5918050 A *	06-29-1999	Rosenthal et al.	
		US- 6223274 B1 *	04-24-2001	Catthoor et al.	
		US- 6023738 A *	02-08-2000	Priem et al.	
		US- 5859797 A *	08-19-1997	Zandveld et al.	
		US- 6070202 A *	05-30-2000	Minkoff et al.	
		US- 8247105 B1 *	06-12-2001	Goldstein et al.	
		US- 5978893 A *	11-02-1999	Bakshi et al.	
		US- 5784699 A *	07-21-1998	McMahon et al.	
		US- 5634015 A *	05-27-1997	Chang et al.	
		US- 5023778 A *	06-11-1991	Gregor, Steven	
		US- 4942518 A *	07-17-1990	Weatherford et al.	
		US- 4707784 A *	11-17-1987	Ryan et al.	
		US- 6009516 A *	12-28-1999	Steiss et al.	
		US- 5812810 A *	09-22-1998	Sager, David	
		US- 5987578 A *	11-16-1999	Butcher, Lawrence	

Examiner Initials*	Cite No. ¹	FOREIGN PATENT DOCUMENTS			
		Foreign Patent Document Country Code ² Number ³ Kind Code ⁴ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear

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Art Unit	2153														
Examiner Name	Glenton Burgess														
Attorney Docket Number	P3842														

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Examiner Signature		Date Considered	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	09/984,827		
		Filing Date	09/25/2001		
		First Named Inventor	Enrique Musoll		
		Art Unit	2153		
		Examiner Name	Glenton Burgess		
Sheet	3	of	3	Attorney Docket Number	P3842

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		* KNUTH, DONALD E., "The Art of Computer Programming, Third Edition, Volume 1, Fundamental Algorithms", "Sec. 2.5 Dynamic Storage Allocation", 1997, pp 435-458, Addison-Wesley, US	
		* DIFENDORFF, Keith, K7 Challenges Intel, Microprocessor Report, October 26, 1998, Vol. 12, No. 14, US	

Examiner Signature	Date Considered
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